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- ☐ 5. **A fully-pipelined single-precision floating point unit in the synergistic processor element of a CELL processor**
Hwa-Joon Oh; Mueller, S.M.; Jacobi, C.; Tran, K.D.; Cottier, S.R.; Michael, B.W.; Nishikawa, H.;

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- └ 6. **Conflict-free access to multiple single-ported register files**
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- └ 7. **A dual mode IEEE multiplier**
Even, G.; Mueller, S.M.; Seidel, P.-M.;
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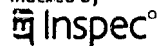
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